AMENDMENTS TO THE DRAWINGS:

The attached sheet(s) of drawing(s) includes changes to Figures 1-4. Figures 1-4 have

been amended to include the designation -- Prior Art--. Figure 2 is further amended to place a

box around switching elements 32a, 32b, and 32c and identify the contents of the box as

switching fabric 34.

A red-lined version of Figures 1-4 is attached illustrating these amendments to Figures 1-

4. Further, replacement sheets 1-12 including Figures 1-6, 7a-7e, and 8a-8c are attached. The

replacement sheets replace all of the figures with more formal drawings and including the

amendments described above.

Attachments:

Replacement Sheet 1-12 including Figures 1-6, 7a-7e, and 8a-8c.

Red-lined Sheets illustrating the amendments made to Figures 1-4.

-14-



PATENT Customer No. 22,852 Attorney Docket No. 9145.0006-00

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:)	
Kenneth BRANTH et al.) Group Art Unit: 2666	
Application No.: 09/670,001) Examiner: Wanda Eugene	
Filed: September 25, 2000 For: DETECTION AND RECOVERY FROM CONNECTION FAILURE IN AN ATM SWITCH)) Confirmation No.: 3652))	JUL 2 1 2004 Technology Center 2600
Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450		
Sir:		

SUBMISSION OF REPLACEMENT DRAWINGS

Subject to the approval of the Examiner, please replace the drawings in the above-identified application with the 12 drawings filed herewith (Figures 1-6, 7a-7e, 8a-8c, and 9-10). If the replacement drawings for any reason are not in full compliance with the pertinent statutes and regulations, please so advise the undersigned.

If any fees are necessary for the submission of these formal drawings, please charge our Deposit Account No. 06-0916.

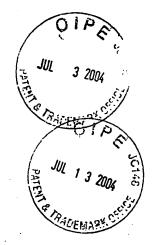
Respectfully submitted,

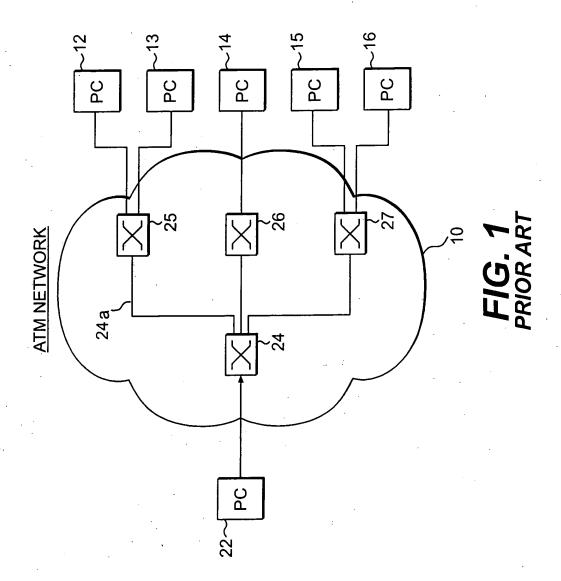
FINNEGAN, HENDERSON, FARABOW, GARRETT & DUNNER, L.L.P.

Dated: July 13, 2004 By:

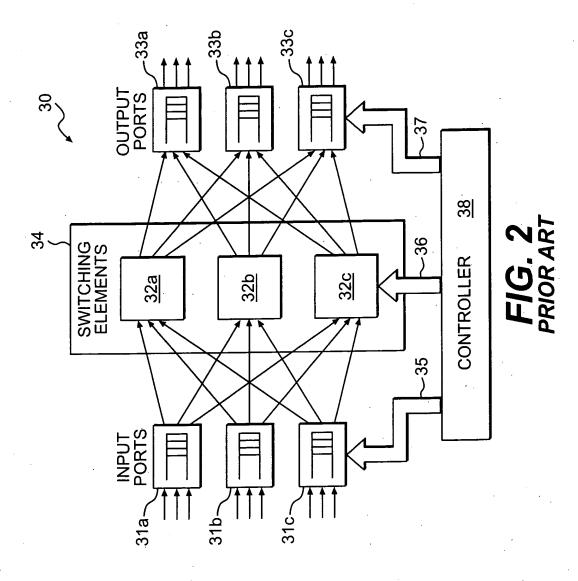
Cary J. Edwards Reg. No. 41,008

EXPRESS MAIL LABEL NO. EV 533068980 US

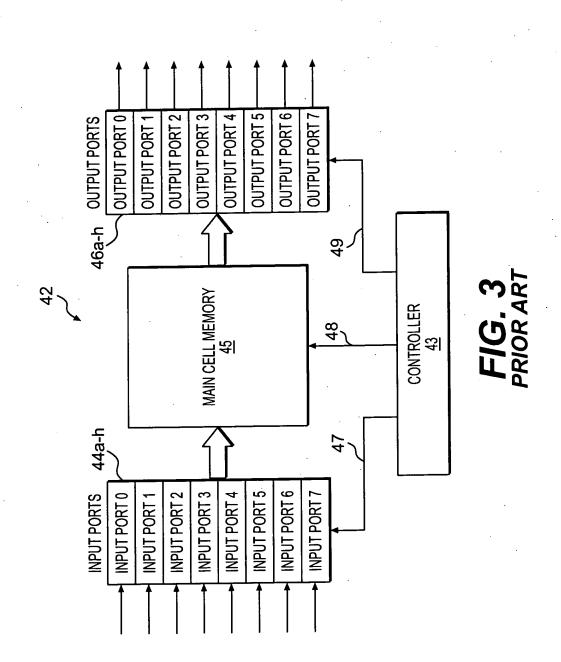














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VC MEMORY	116	00010000	10011111			DVCI OUTPUT PORT#	DVCI OUTPUT PORT#		DVPI DVCI OUTPUT PORT# • •
	114	DVCI	DVCI			DVCI	DVCI		DVCI
	112	DVPI	DVPI		•	DVPI	DVPI	• • •	
,	101	3	35		5	<u> </u>	104	:5	
MULTICAST GROUP									

FIG. 4 PRIOR ART



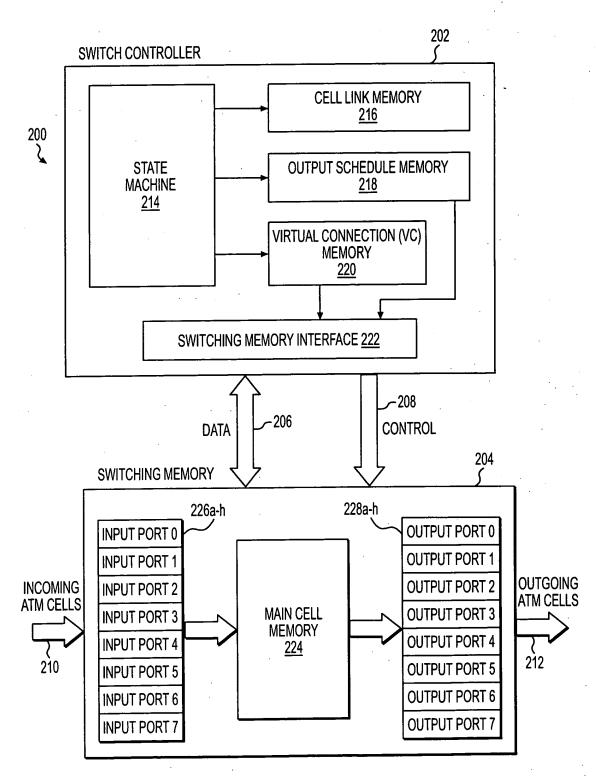
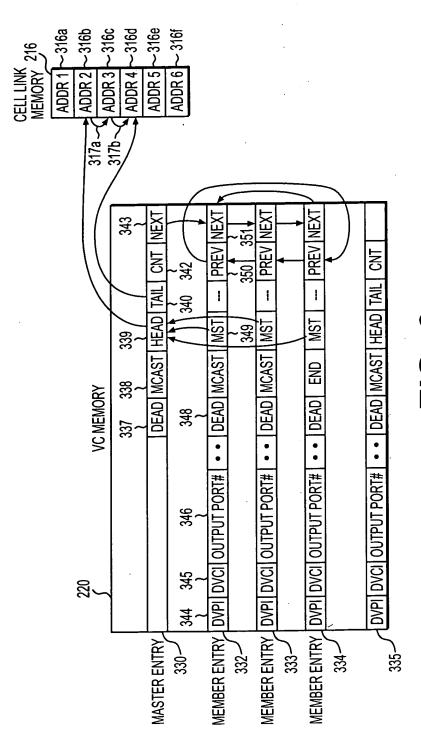


FIG. 5





F/G. 6



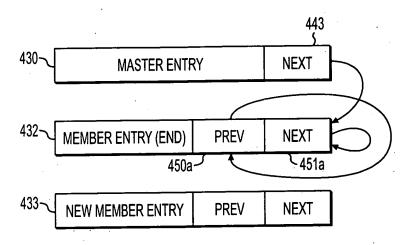


FIG. 7a

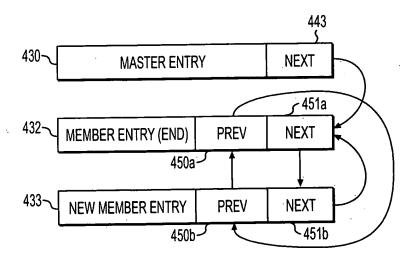


FIG. 7b



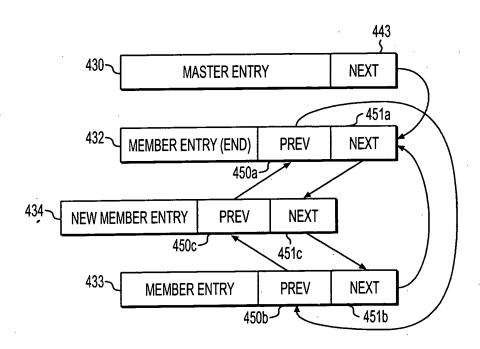


FIG. 7c



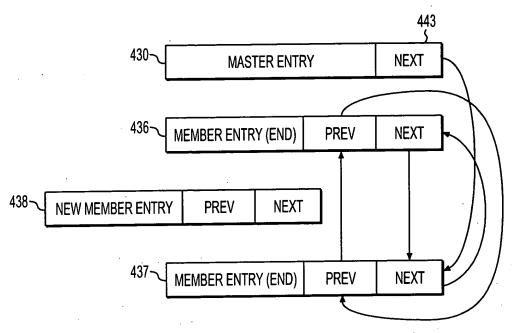


FIG. 7d

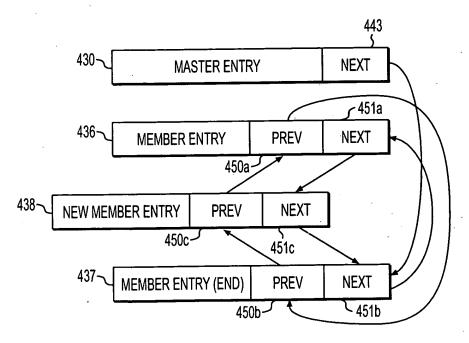
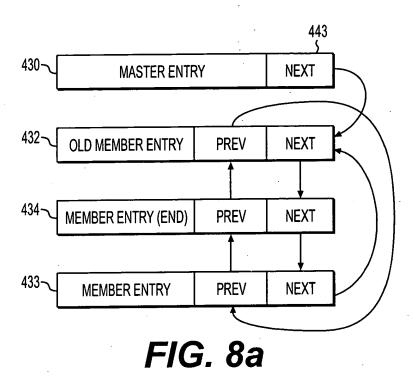
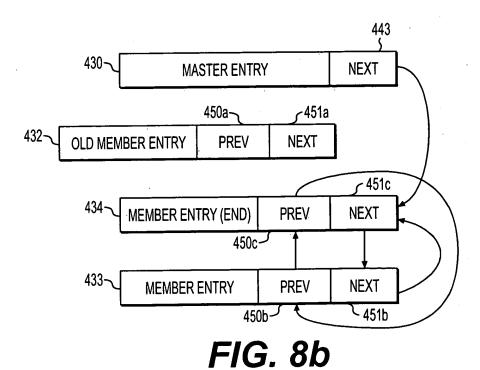


FIG. 7e









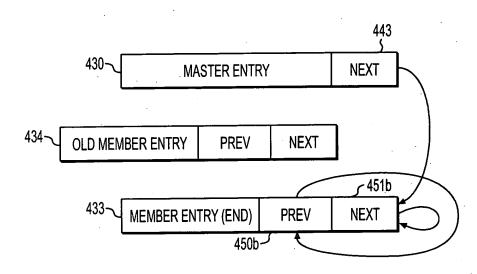


FIG. 8c



	VC MEMORY								520 {	543 }		
MASTER ENTRY		DEAD IM	DEAD CNT		• •	DEAD	MCAST	HEAD	TAIL	CNT	NEXT	
530		554	556			548 }			. 5	550	1	$\left\{ \right.$
MEMBER ENTRY	DVPI	DVCI	SPORT	SRV	• •	DEAD	MCAST	MŚT		PREV	NEXT	1
532				•			538	1		1 :	551¥	
MEMBER ENTRY	DVPI	DVCI	SPORT	SRV	• •	DEAD	MCAST	MSŤ		PREV	NEXT	
533								\			<u> </u>]/
MEMBER ENTRY	DVPI	DVCI	SPORT	SRV	• •	DEAD	MCAST	MST		PREV	NEXT	Y /
534										•		
FIG. 9												

629 .					657 ر	658 ر	620			
4	DVPI	DVCI	OUTPU	T PORT#	DEAD-ITM	DEAD-CNT		Н	Τ	С
030	630 DVPI		OUTPU	T PORT #	DEAD-ITM	DEAD-CNT	• • •	Н	Т	С
	٠		•	618	654	656				
ربر	DVPI	DVCI	OUTPU	T PORT#						
632 رىم	DVPI	DVCI	OUTPU	T PORT#						
633										

FIG. 10